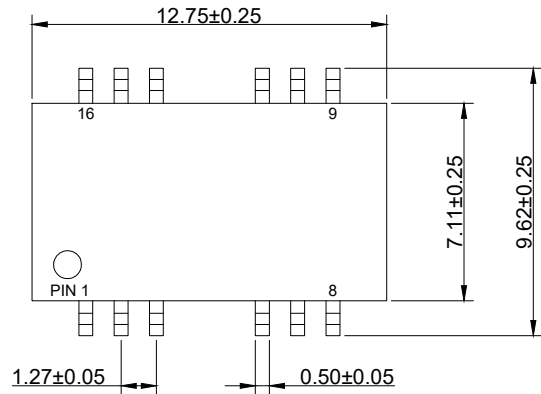
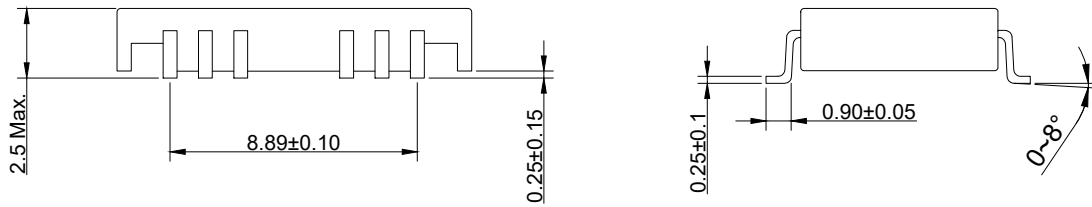


1 2 3 4 5 6

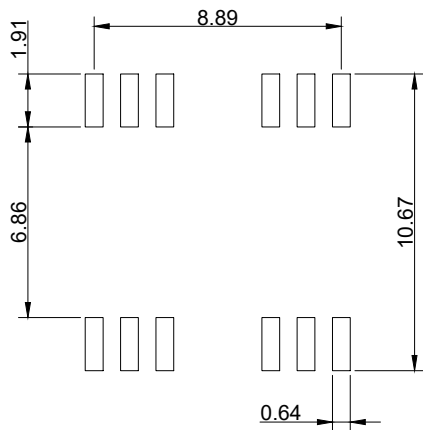
A



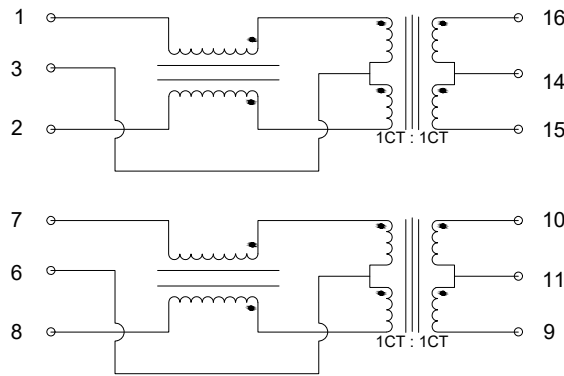
B



C



RECOMMENDED PCB LAYOUT



CIRCUIT SCHEMATIC

PARAMETER	SPECIFICATIONS	
	TURNS RATIO	(1-2) : (16-15)
(7-8) : (10-9)		1:1 $\pm 3\%$
INDUCTANCE	(1-2) : (7-8)	365uH MIN. @ 0.1V 100KHz 8mA DC BIAS
	LEAKAGE INDUCTANCE	
LEAKAGE INDUCTANCE	1-2 BY 16&15 SHORT	0.35uH MAX @1MHz
	7-8 BY 10&9 SHORT	0.35uH MAX @1MHz
INTERWINDING CAPACITANCE	(1-2) : (16-15)	27pF MAX @1MHz
	(7-8) : (10-9)	27pF MAX @1MHz
DC RESISTANCE	(16-15) : (10-9)	1.1 Ω MAX.
RETURN LOSS	1-30MHz	-18dB Min.
	30-80MHz	-12dB Min.
DIELECTRIC WITHSTAND	(1-2) : (16-15)	1600V AC
	(6-8) : (11-9)	1600V AC
INSERTION LOSS	100KMz-100MHz	-1.0dB Max.
CROSS TALK	1-100MHz	35dB Min.
COMMON TO COMMON MODE ATTENUATION	30-100MHz	30dB Min.
OPERATING TEMPERATURE	0 ~ 70°C	
STORAGE TEMPERATURE	-40°C ~ 85°C	

A

B

C

D

1 2 3 4 5 6

		UNIT	mm
		REVISION	X
PRODUCT		LAN FILTER	
DWG NO.		PART NO.	
EZCB1201-01			
PROJECTION	SCALE	SHEET	1 OF 1

UNIT
mm
REVISION
X
TOLERANCES
 ± 0.25
UNLESS OTHERWISE
SPECIFIED