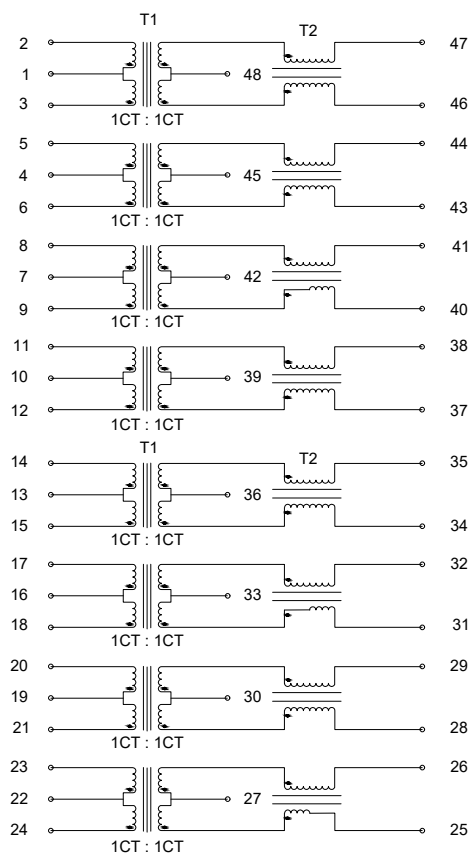
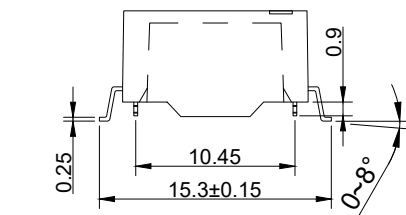


RECOMMENDED PCB LAYOUT



CIRCUIT SCHEMATIC

PARAMETER	SPECIFICATIONS	
URNS RATIO	(2-3) : (47-46) (5-6) : (44-43) (8-9) : (41-40) (11-12) : (38-37) (14-15) : (35-34) (17-18) : (32-31) (20-21) : (29-28) (23-24) : (26-25)	1:1 ±3%
INDUCTANCE	(2-3),(5-6),(8-9) (11-12),(14-15),(17-18) (20-21),(23-24)	365uH MIN. @ 0.1V 100KHz 8mA DC BIAS
LEAKAGE INDUCTANCE	2-3 BY 47&46 SHORT 5-6 BY 44&43 SHORT 8-9 BY 41&40 SHORT 11-12 BY 38&37 SHORT 14-15 BY 35&34 SHORT 17-18 BY 32&31 SHORT 20-21 BY 29&28 SHORT 23-24 BY 26&25 SHORT	0.35uH MAX @1MHz
INTERWINDING CAPACITANCE	(2-3) : (47-46) (5-6) : (44-43) (8-9) : (41-40) (11-12) : (38-37) (14-15) : (35-34) (17-18) : (32-31) (20-21) : (29-28) (23-24) : (26-25)	27pF MAX @1MHz
DC RESISTANCE	(47-46) : (44-43) : (41-40) : (38-37) : (35-34) : (32-31) : (29-28) : (26-25)	1.1Ω MAX.
RETURN LOSS	1-30MHz 30-80MHz	-16dB Min. -12dB Min.
DIELECTRIC WITHSTAND	(2-3) : (47-46) (5-6) : (44-43) (8-9) : (41-40) (11-12) : (38-37) (14-15) : (35-34) (17-18) : (32-31) (20-21) : (29-28) (23-24) : (26-25)	1600V AC
INSERTION LOSS	100KMz-100MHz	-1.2dB Max.
CROSS TALK	1-100MHz	-35dB Min.
COMMON TO COMMON MODE ATTENUATION	30-100MHz	-30dB Min.
OPERATING TEMPERATURE	0 ~ 70°C	
STORAGE TEMPERATURE	-25°C ~ 85°C	

PRODUCT		LAN FILTER		UNIT	mm
DWG NO.		PART NO.		REVISION	X
EZCB4802-01				TOLERANCES	± 0.25 UNLESS OTHERWISE SPECIFIED
PROJECTION	SCALE	SHEET		1 OF 1	